

CLAIMS:

- What is claimed is:

- 1 1. A device comprising:
2 a first substrate coupled to a second substrate;
3 the first substrate comprising a plurality of display blocks which are
4 deposited onto said first substrate and an integrated circuit coupled to the
5 display blocks;
6 the integrated circuit, configured to receive a signal from an external
7 source; and
8 a single I/O coupled to at least one display block and a chip.
- 1 2. The device of claim 1, wherein the second substrate comprises one of a
2 flexible layer and a rigid layer.
- 1 3. The device of claim 1, wherein the integrated circuit comprises at least
2 one driver block deposited onto the first substrate, wherein the driver block is
3 coupled to at least one display block.
- 1 4. The device of claim 1, wherein each of said shaped display blocks
2 comprises an active circuit element which drives a picture element.
- 1 5. The device of claim 1, wherein the first substrate comprises an active
2 matrix backplane, the device further comprises:
3 a display generation substrate coupled to an active matrix backplane.
- 1 6. The device of claim 5, wherein the device has liquid crystal.
- 1 7. The device of claim ~~5~~, wherein the device has at least one OLED.
- 1 8. The device of claim 1, wherein said first substrate has an active matrix
2 backplane which comprises at least one electrode for each picture element.
- 1 9. The device of claim 5, wherein said active matrix display is conformal.
- 1 10. The device of claim 1, wherein at least one of the first substrate and the
2 second substrate is flexible.

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- 1 11. An apparatus comprising:
2 at least one pixel block onto a substrate, said at least one pixel block
3 connected to a pixel element;
4 depositing at least one interface block onto said substrate;
5 coupling electrically said at least one pixel block and interface block to
6 form an active matrix backplane;
7 wherein said display blocks have an integrated circuit thereon;
8 transferring data to at least one integrated circuit.
- 1 12. The method of claim 11, wherein each of said display blocks comprises
2 an active circuit element which drives a picture element.
- 1 13. The method of claim 11, further comprising:
2 coupling a display generation substrate coupled to said active matrix
3 backplane.
- 1 14. The method of claim 11, wherein said active matrix display backplane
2 comprises at least one electrode for each picture element.
- 1 15. The method of claim 11, wherein the flexible active matrix display panel
2 comprises a single crystal silicon transmissive display.
- 1 16. The method of claim 11, wherein the flexible active matrix display panel
2 comprises a single crystal silicon reflective display.
- 1 17. The method of claim 11, wherein the flexible active matrix display panel
2 comprises an organic light emitting diode.
- 1 18. The method of claim 11, wherein the flexible active matrix display panel
2 comprises upconverting phosphor.
- 1 19. A device comprising:
2 a first substrate;
3 a second substrate coupled to the first substrate;
4 the first substrate comprising a plurality of blocks which are deposited
5 onto said substrate and an integrated circuit, configured to receive a signal
6 from an external source; and

7 less than or equal to four I/Os coupled to at least one display block and
8 a chip.

1 20. A device comprising:

2 a first substrate;

3 a second substrate coupled to the first substrate;

4 the first substrate comprising a plurality of blocks which are deposited

5 onto said substrate and an integrated circuit, configured to receive a signal

6 from an external source; and

7 less than or equal to three I/Os coupled to at least one display block and

8 a chip.

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